

AMENDMENTS TO THE CLAIMS

**List of Claims:**

1. (Previously Presented) A charge characteristic compensating circuit for a liquid crystal display panel including a plurality of liquid crystal cells arranged at each intersection between data lines and gate lines to control a light transmissivity in response to data signals from the data lines, and a plurality of switching devices for switching the data signals to be applied from the data lines to the liquid crystal cells in response to signals on the gate lines, the circuit comprising:

a voltage supply for generating a gate voltage required for the gate lines;

a gate line driver for applying the gate voltage from the voltage supply to the gate lines to drive the gate lines; and

a current controller including a resistor and a thermistor for responding to a change in the ambient temperature to change an amount of current of the gate voltage to be applied from the voltage supply to the gate line driver, thereby changing a width of a current path from the data line to the liquid crystal cell.

2. (Previously Presented) The charge characteristic compensating circuit as claimed in claim 1, wherein said resistor and said thermistor are connected, in parallel, between the voltage supply and the gate line driver.

3. (Previously Presented) The charge characteristic compensating circuit as claimed in claim 1, wherein said resistor and said thermistor are connected, in series, between the voltage supply and the gate line driver.

4. (Original) The charge characteristic compensating circuit as claimed in claim 2, wherein the thermistor is a positive temperature coefficient thermistor.

5. (Original) The charge characteristic compensating circuit as claimed in claim 3, wherein the thermistor is a positive temperature coefficient thermistor.

6. (Previously Presented) A charge characteristic compensating circuit for a liquid crystal display panel including a plurality of liquid crystal cells arranged at each intersection between data lines and gate lines to control a light transmissivity in response to data signals from the data lines, and a plurality of switching devices for switching the data signals to be applied from the data lines to the liquid crystal cells in response to signals on the gate lines, the circuit, comprising:

a voltage supply for generating a gate voltage required for the gate lines;

a gate line driver for applying the gate voltage from the voltage supply to the gate lines to drive the gate lines; and

a current controller including a resistor and a thermistor for responding to a change in the ambient temperature to change a voltage level of the gate voltage to

be applied from the voltage supply to the gate line driver, thereby changing a width of a current path from the data line to the liquid crystal cell.

7. (Previously Presented) The charge characteristic compensating circuit as claimed in claim 6, wherein the current controller includes a resistive voltage divider connected between the voltage supply and the gate line driver and composed of said resistor and said thermistor.

8. (Original) The charge characteristic compensating circuit as claimed in claim 6, wherein the thermistor is a negative temperature coefficient thermistor.

Claims 9-26 (CANCELLED)